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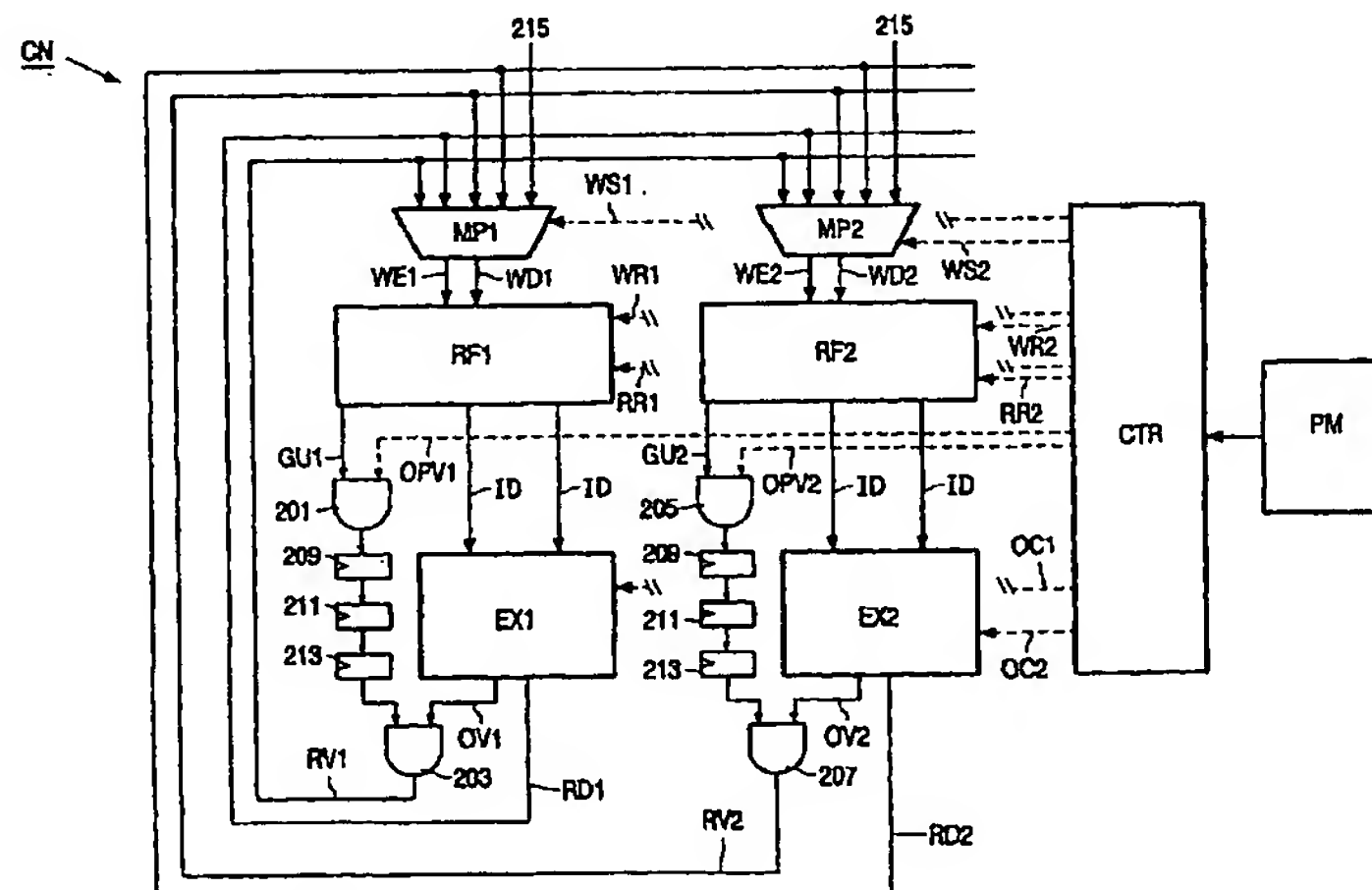
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[Continued on next page]

(54) Title: SUPPORT FOR CONDITIONAL OPERATIONS IN TIME-STATIONARY PROCESSORS



(57) Abstract: In case of time-stationary encoding, every instruction that is part of the processor's instruction-set controls a complete set of operations that have to be executed in a single machine cycle. These operations may be processing several different data items traversing the data pipeline. Time-stationary encoding is often used in application-specific processors, since it saves the overhead of hardware necessary for delaying the control information present in the instructions, at the expense of larger code size. A disadvantage of time-stationary encoding is that it does not support conditional operations. The invention proposes to dynamically control the write back of result data to the register file of the time-stationary processor, using control information obtained by the program. By controlling the write back of data at run-time, conditional operations can be implemented by a time-stationary processor.

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A. CLASSIFICATION OF SUBJECT MATTER  
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## B. FIELDS SEARCHED

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the International search (name of data base and, where practical, search terms used)

EPO-Internal

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2002/056034 A1 (GEARTY MARGARET ET AL) 9 May 2002 (2002-05-09) paragraph '0048! - paragraph '0049! paragraph '0055! -----	1,2,4, 6-8
X	WO 01/61478 A (KONINKLIJKE PHILIPS ELECTRONICS N.V) 23 August 2001 (2001-08-23) page 5, line 24 - line 32 -----	1,8
A	US 5 600 848 A (SPROULL ET AL) 4 February 1997 (1997-02-04) claims 10,12 -----	1,8

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☒ Patent family members are listed in annex.

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information on patent family members

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